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REMARKS

In a final office action dated 3 October 2003, the Examiner objects to the drawings, specification and claims. The Examiner also rejects claims 1-7. In response to the office action, Applicants amend the drawings, specification, and claims 1-7. Applicants also respectfully traverse the rejections. In light of the amendments and arguments sent forth below, Applicants respectfully request that this application be allowed.

Applicants have amended the drawings and specification to address the deficiencies cited by the Examiner. Specifically, Applicants have amended the drawings to include the unlabeled I/O in FIGS. 6 and 13. Applicants have also included red-lined drawings of FIGS. 16A and 16B to include connections of tri-state buffers. Furthermore, Applicants have amended the abstract to conform to USPTO rules.

Therefore, Applicants respectfully request the objections to the drawings be removed.

The Examiner has objected the claims 1-7. Applicants have amended claim 1 to omit any reference to FPGA. Therefore, Applicants request this objection be removed.

The Examiner rejects claims 6 and 7 under 35 U.S.C. §112(2). Applicant has amended the claims to better describe the invention and more particularly to include connection of the input/output and RAM to the field programmable gate array. Therefore, Applicant respectfully request these rejections be removed.

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The Examiner rejects claim 1 under 35 U.S.C. §102(e) as being anticipated by U.S. Patent Number 6,285,212 issued to Kaptanoglu (K). To anticipate a claim under 35 U.S.C. § 102, a single source must contain all of the elements of the claim. *Lewmar Marine Inc. v. Barient, Inc.*, 827 F.2d 744, 747, 3 U.S.P.Q.2d 1766, 1768 (Fed. Cir. 1987), cert. denied, 484 U.S. 1007 (1988). Moreover, the single source must disclose all of the claimed elements "arranged as in the claim." *Structural Rubber Prods. Co. v. Park Rubber Co.*, 749 F.2d 707, 716, 223 U.S.P.Q. 1264, 1271 (Fed. Cir. 1984).

Claim 1 recites a freeway routing system wherein the freeway routing system connects interface groups in said field programmable gate array. The freeway routing system comprises vertical and horizontal conductors connected by programmable elements arranged in a diagonal. K does not teach this limitation. K teaches an expressway routing system for connecting B1 blocks in adjacent B16XB16 tiles. B1 blocks include four clusters of logic elements. See Col. 9, lines 29-44. All the connections taught in K relate to the expressway connections and do not teach anything relating to the freeway connections. In fact the only discussion of freeway connections in K is given in at Col. 2, lines 27-37 and Col. 4, lines 31-62. In these sections, there is no mention of conductors and programmable elements of the freeway system as recited in claim 1. Thus, K does not teach the element of claim 1 as recited in the claim. Thus, Applicants respectfully request that the rejection to claim 1 be removed and amended claim 1 be allowed.

Claims 2-7 are dependent upon amended claim 1. Thus, claims 2-7 are allowable as being dependent upon an allowable independent claim. Therefore, Applicants respectfully request that claims 2-7 be allowed.

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If the Examiner has any questions regarding this response or the application in general, the Examiner is invited to telephone the undersigned at 775-586-9500.

Respectfully submitted, SIERRA PATENT GROUP, LTD.

Dated: June 21, 2004

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